

ABSTRACT OF THE DISCLOSURE

A level-shifting circuit comprising an enable circuit is disclosed. The level modulating circuit includes an input terminal and an inverse input terminal for respectively
5 receiving a complementary pair of small signals, and a first output terminal for outputting a voltage level in response to the complementary pair of small signals. The enable circuit is coupled to the first output terminal and makes the first output terminal output a predetermined voltage level signal when
10 receiving a disable signal.